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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Applicati n Numb r	09/118,359	
	Filing Date	July 17, 1998	
	First Named Inventor	J. Dennis Keller et al.	
	Group Art Unit	2811	
	Examiner Name	C. Nguyen	
Total Number of Pages in This Submission	7	Attorney Docket Number	MI22-587

ENCLOSURES (check all that apply)		
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Firm or Individual name	Lance R. Sadler Wells, St. John, Roberts, Gregory & Matkin, P.S. 601 West First Ave, Suite 1300, Spokane, WA 99201-3828
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Practitioner's Docket No. MI22-587



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Keller et al., J. Dennis

Serial No.: 09/118,359

Group No.: 2811

Filed: 07/17/1998

Examiner: C. Nguyen

For: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of Forming Floating Gate Transistors, and Floating Gate Transistors

Assistant Commissioner for Patents
Washington, D.C. 20231

REQUEST FOR CORRECTED FILING RECEIPT

1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
2. There is an error with respect to the following, which is incorrectly entered.

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11/20/98

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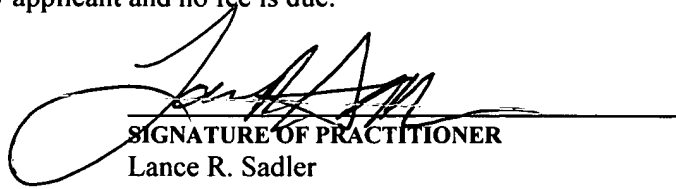
Error in

1. In the title, the last occurrence of the word "Trnsistors" should be replaced with -- Transistors--.

Correct data

1. Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of Forming Floating Gate Transistors, and Floating Gate Transistors

3. The correction is not due to any error by applicant and no fee is due.

A handwritten signature in black ink, appearing to read "Lance R. Sadler", is written over a horizontal line. The signature is stylized with a large loop at the beginning and a long horizontal stroke extending to the right.

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Lance R. Sadler

Reg. No.: 38605

Tel. No.: (509) 624-4276

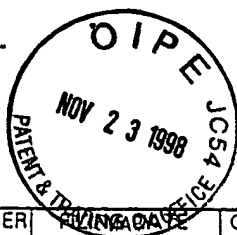
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
09/118,359	07/17/98	2811	\$1,558.00	MI22-587	4	40	7

021567
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Applicant(s)

J. DENNIS KELLER, BOISE, ID; ROGER R. LEE, BOISE, ID.

FOREIGN FILING LICENSE GRANTED 08/18/98

TITLE

METHODS OF ENHANCING DATA RETENTION OF A FLOATING GATE TRANSISTOR,
METHODS OF FORMING FLOATING GATE TRANSISTORS, AND FLOATING GATE
TRANSISTORS TRANSISTORS

PRELIMINARY CLASS: 257

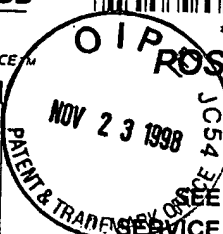
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Jc135 U.S. PTO
09/118359MI22-587 (lrs)
Keller et al.

07/17/98



07/17/98

The stamp of the U.S. Patent and Trademark Office acknowledges receipt of the application for letters patent of the following:

Applicant: J. Dennis Keller et al.

Titled: "Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of Forming Floating Gate Transistors, and Floating Gate Transistors"

1. Return Postcard Receipt
2. A \$1,598.00 check
3. PTO Utility Patent Application Transmittal
4. PTO Fee Transmittal in duplicate
5. Title page, plus 23 total Specification, Claims and Abstract pages (Claims 1-40)
6. Executed Declaration
7. 4 sheets of formal drawings (Figs. 1-8)
8. Assignment of the invention to Micron Technology, Inc., and Recordation Cover Sheet.
9. Power of Attorney

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTORSHIP J. Dennis Keller et al.
ASSIGNEE Micron Technology, Inc.
ATTORNEY'S DOCKET NO. MI22-587
TITLE: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of
Forming Floating Gate Transistors, and Floating Gate Transistors

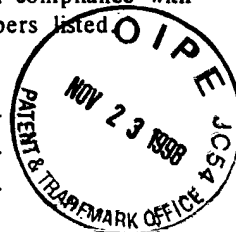
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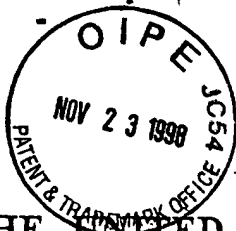
Listing Of Items Enclosed In Mailing:

1. Return Postcard Receipt
2. A \$1,598.00 check
3. PTO Utility Patent Application Transmittal
4. PTO Fee Transmittal in duplicate
5. Title page, plus 23 total Specification, Claims and Abstract pages (Claims 1-40)
6. Executed Declaration
7. 4 sheets of formal drawings (Figs. 1-8)
8. Assignment of the invention to Micron Technology, Inc., and Recordation Cover Sheet.
9. Power of Attorney

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statement may jeopardize the validity of the application or any patent issued therefrom.

Dated this 17 day of July, 1998.
Dated at Spokane, Washington

Jami R. Inaba
(Signature)



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APPLICATION FOR LETTERS PATENT

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* * * * *

Methods Of Enhancing Data Retention Of A
Floating Gate Transistor, Methods Of Forming
Floating Gate Transistors, And Floating Gate
Transistors

* * * * *

INVENTORS:

J. Dennis Keller
Roger R. Lee

ATTORNEY'S DOCKET NO. MI22-587

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